

Ingress

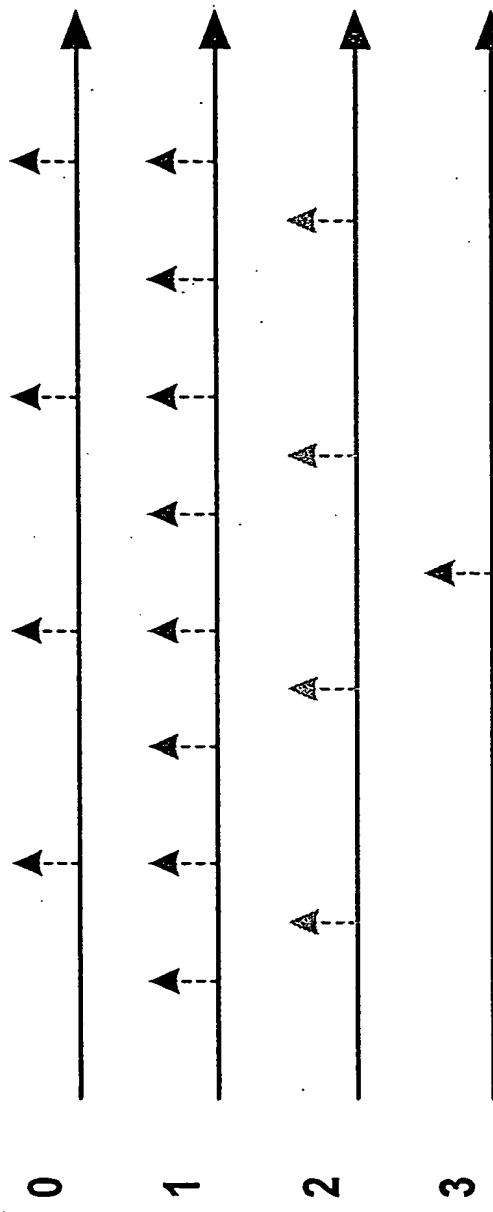


Fig. 1a

Multicast Schedule

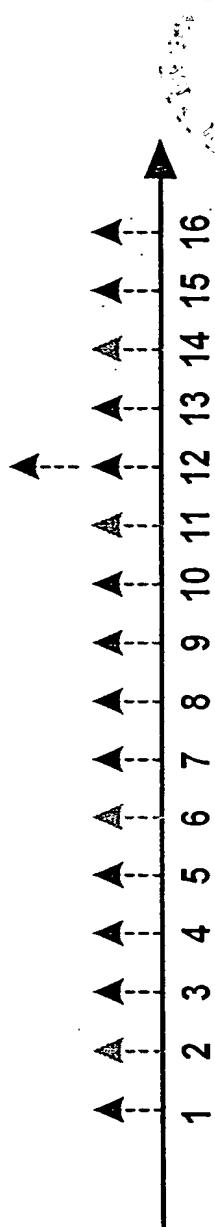


Fig. 1b

Fig. 2

Multicast Egress Credit		
27	0	35
5		

Fig. 2b

Fig. 2e

Fig. 2d

2/2

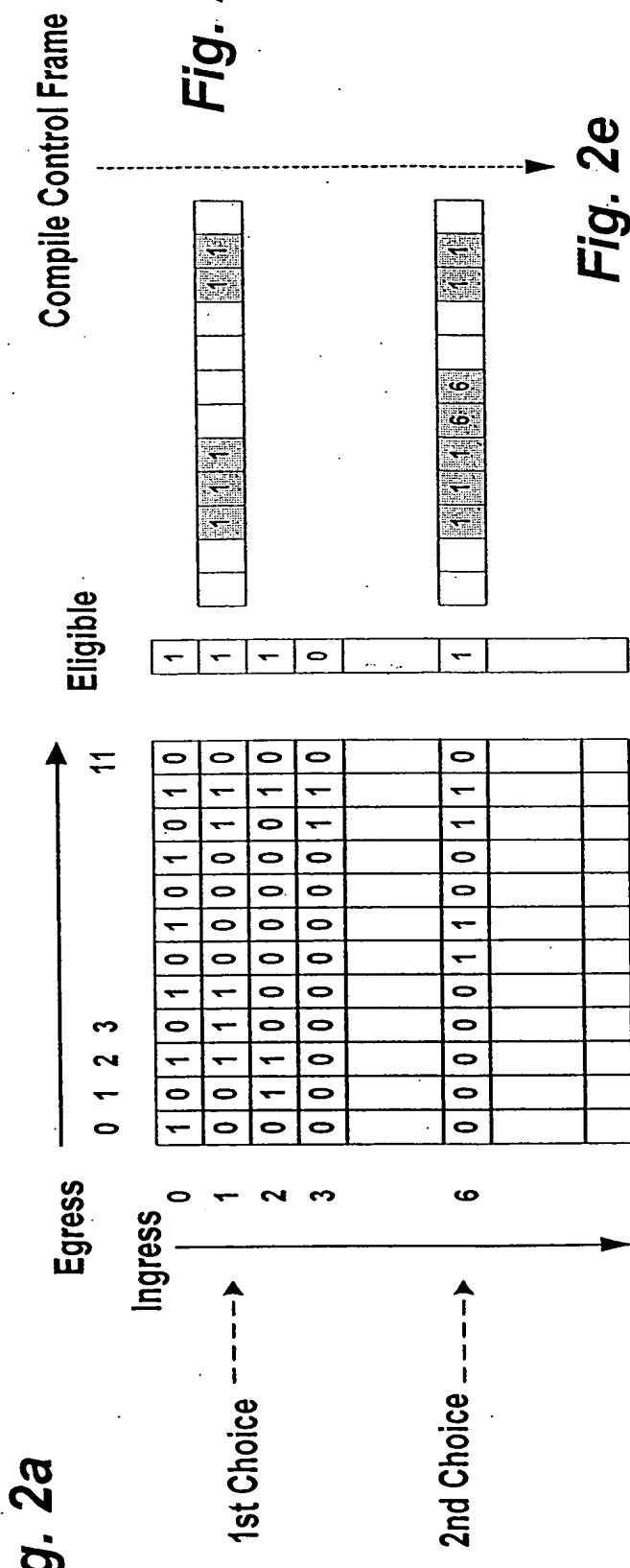


Fig. 2c

Compile Control Frame

Compile Control Frame											
1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1
0	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1

Fig. 2d

Compile Control Frame

Fig. 2e